

UF200-UF2010

2.0A Axial Leaded Ultrfast Switching Fast Rectifier

Features

- Plastic package has Underwriters Laboratory Flammability Classification 94V-O utilizing Flame Retardant Epoxy Molding Compound
- Void-free Plastic in DO-15 package
- 2.0 ampere operation at T_A=55 ¢J with no thermal runaway
- Exceeds environmental standards of MIL-S-19500/228
- Ultra fast switching for high efficiency

Mechanical Data

• Case: Molded plastic, DO-15

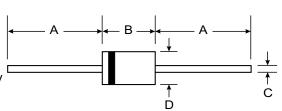
• Terminals: Axial leads, solderable per MIL-STD-202,

Method 208

• Polarity: Band denotes cathode

• Mounting Position: Any

• Weight: 0.015 ounce, 0.4 gram



DO-15						
Dim	Min	Max				
Α	25.40	_				
В	5.50	7.62				
С	0.686	0.889				
D	2.60	3.60				
All Dimensions in mm						

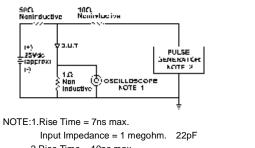
Maximum Ratings and Electrical Characteristics @ T_A = 25°C unless otherwise specified

Single phase, half wave, 60Hz, resistive or inductive load. For capacitive load, derate current by 20%.

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UF200	UF201	UF202	UF204	UF206	UF208	UF2010	UNITS
50	100	200	400	600	800	1000	V
35	70	140	280	420	560	700	V
50	100	200	400	600	800	1000	V
" 2.0						Α	
60						Α	
	1.00		1.10		1.70		V
10.0						£g A	
500						£g A	
35						₽F	
45						¢J/W	
50	50	50	50	75	75	75	ns
-55 TO +150						¢J	
	50 35 50	50 100 35 70 50 100	50 100 200 35 70 140 50 100 200	50 100 200 400 35 70 140 280 50 100 200 400 2.0 60 1.00 1.10 10.0 500 35 45 50 50 50	50 100 200 400 600 35 70 140 280 420 50 100 200 400 600 2.0 60 1.00 1.10 10.0 500 35 45 45 50 50 50 50 75	50 100 200 400 600 800 35 70 140 280 420 560 50 100 200 400 600 800 2.0 60 1.00 1.10 1.70 10.0 500 500 35 45 50 50 50 75 75	50 100 200 400 600 800 1000 35 70 140 280 420 560 700 50 100 200 400 600 800 1000 2.0 60 1.00 1.10 1.70 500 500 35 45 50 50 75 75 75

NOTES:

- Measured at 1 MHz and applied reverse voltage of 4.0 VDC
- 2. Thermal resistance from junction to ambient and from junction to lead length 0.375"(9.5mm) P.C.B. mounted



2.Rise Time = 10ns max. Source Impedance = 50 Ohms

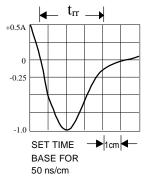


Fig. 1-REVERSE RECOVERY TIME CHARACTERISTIC AND TEST CIRCUIT DIAGRAM

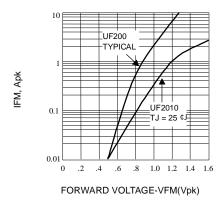


Fig. 2-FORWARD CHARACTERISTICS

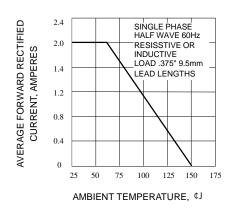


Fig. 3-FORWARD CURRENT DERATING CURVE

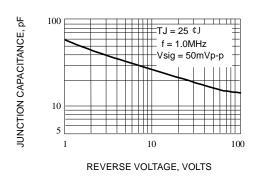


Fig. 4-TYPICAL JUNCTION CAPACITANCE

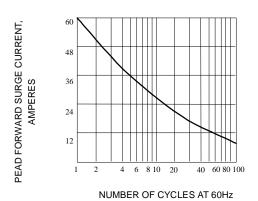


Fig. 5-PEAK FORWARD SURGE CURRENT